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Substitute for form 1449/PTO				<i>Complete if Known</i>	
				Application Number	10/695,498
				Filing Date	October 29, 2003
				First Named Inventor	Abbas Amirichimeh
				Art Unit	2609
				Examiner Name	Alia, Curtis A.
Sheet	1	of	1	Attorney Docket Number	1875.3640003/JTH/TAD

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)	MM-DD-YYYY		
FP	US1	4,833,605	05-23-1989	Terada et al.	
	US2	4,833,695	05-23-1989	Greub	
	US3	6,259,693 B1	07-10-2001	Ganmukhi et al.	
	US4	6,317,804 B1	11-13-2001	Levy et al.	
	US5	6,483,849 B1	11-19-2002	Bray et al.	
	US6	7,032,139 B1	04-18-2006	Iryami et al.	
	US7	7,035,228 B2	04-25-2006	Baumer	
	US8				
	US9				
	US10				
	US11				
	US12				
	US13				
	US14				
	US15				
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FOREIGN PATENT DOCUMENTS					
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	FP1				
	FP2				
	FP3				
	FP4				
	FP5				
	FP6				
	FP7				
	FP8				
	FP9				

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Examiner Signature	/Raj Jain/	Date Considered	05/05/2008
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Substitute for form 1449/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				<i>Application Number</i>	10/695,498
				<i>Filing Date</i>	October 29, 2003
				<i>First Named Inventor</i>	Abbas Amrichimeh
				<i>Art Unit</i>	2609
				<i>Examiner Name</i>	Alia, Curtis A.
Sheet	1	of	1	<i>Attorney Docket Number</i>	1875.3640003/JTH/TAD

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
AV	NPL1	International Search Report issued December 14, 2004, for International Application No. PCT/US003/34234, 5 pages.	
	NPL2	<i>Alaska Quad-Port Gigabit Ethernet Over Copper Transceivers</i> , from http://www.marvell.com/products/transceivers/quadport/ ..., 1 page, printed May 8, 2007.	
	NPL3	<i>Alaska X10 Gigabit Ethernet Alaska Gigabit Ethernet Fast Ethernet Physical Layer (PHY) Transceiver Families Provide a Full Range of Ethernet Transceiver Solutions for the Broadband Communications Industry</i> , from http://www.nuhorizons.com/FeaturedProducts/Volume3/Marvell/phystransceiver.asp , 4 pages, Copyright 2007, printed May 8, 2007.	
	NPL4	<i>Cisco Search: Results for "cdl" within "News@Cisco"</i> , from http://www.cisco.com/cgi-bin/search/search.pl . Copyright 1992-2003, printed July 29, 2003.	
	NPL5	<i>Industry Breakthrough: Marvell Announces the First Quad-Port Transceiver to Support Both Copper and Fiber-Optic Gigabit Ethernet Interfaces</i> , from Business Wire at www.encyclopedia.com/printable/aspx?id=1G1:68912211 , 4 pages, January 9, 2001, printed May 8, 2007.	
	NPL6	<i>David Maliniak (ed.), Bel's integrated connector modules support Marvell's Alaska quad Gigabit Ethernet transceiver</i> , from http://www.electronicsweb.com/Content/news/ ..., 1 page, December 14, 2000, printed May 8, 2007.	
	NPL7	<i>Marvell Gets Small</i> , from http://www.lightreading.com/document.asp?doc_id=12004&print=true , 1 page, February 19, 2002, printed May 8, 2007.	
	NPL8	<i>Marvell Introduces the Industry's Smallest Quad-Port Gigabit Transceiver Device, Enabling Ultra High Port Density Enterprise Switching Systems</i> , from http://www.marvell.com/press/pressNewsDisplay.do?releaseID=41 , 3 pages, February 19, 2002, printed May 8, 2007.	
	NPL9	<i>Q&A: Hiroshi Suzuki on Extending Ethernet Beyond the LAN</i> , from http://newsroom.cisco.com/dlls/innovators/optical/hiroshi_suzuki_qa.html , 3 pages, Copyright 1992-2003, printed July 29, 2003.	
↓	NPL10	<i>Ed Turner and David Law, IEEE P802.3ae MDC/MDIO</i> , from http://www.ieee802.org/3/efm/public/sep01/turner_1_0901.pdf , 21 pages, Sept. 17-19, 2001.	

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